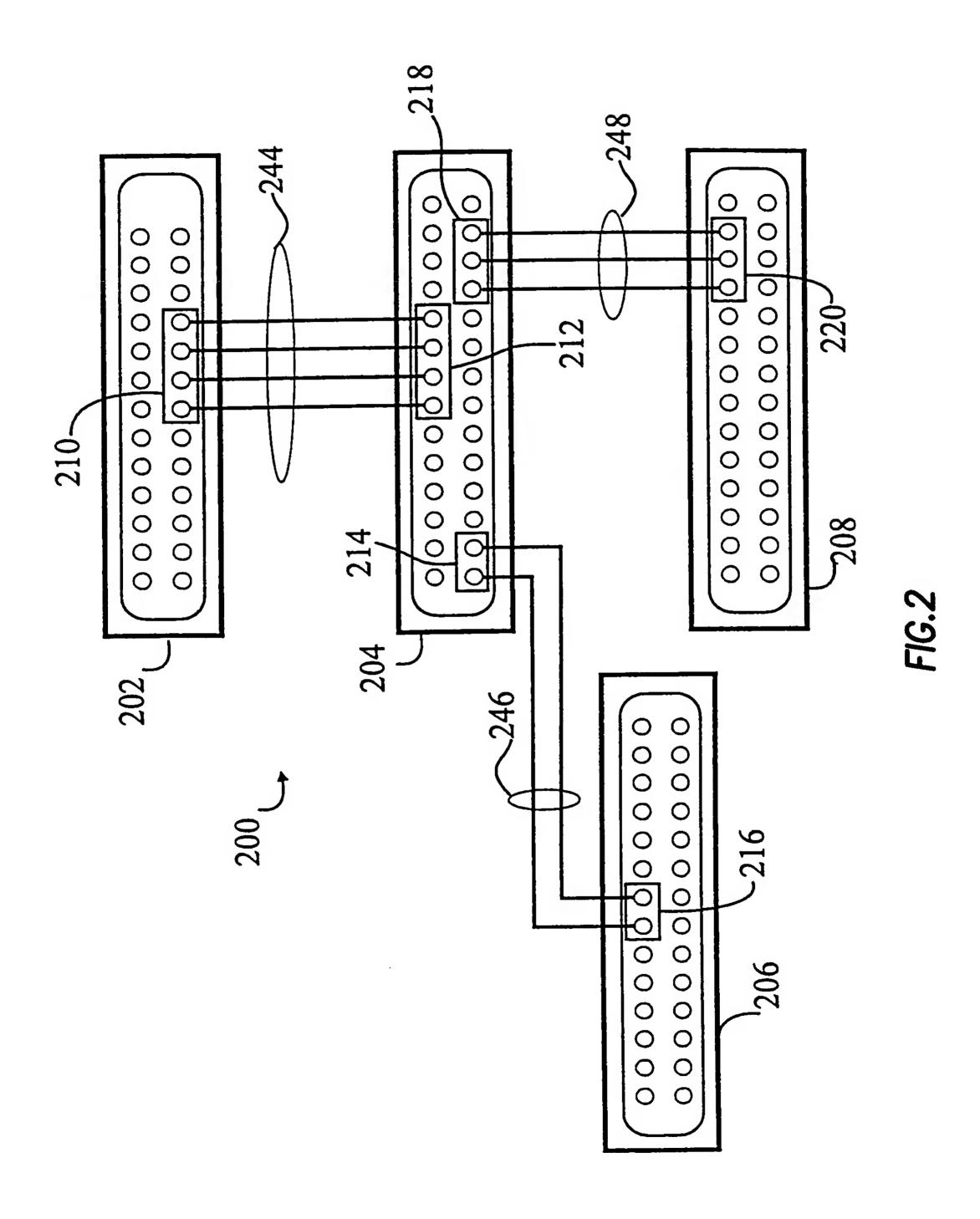
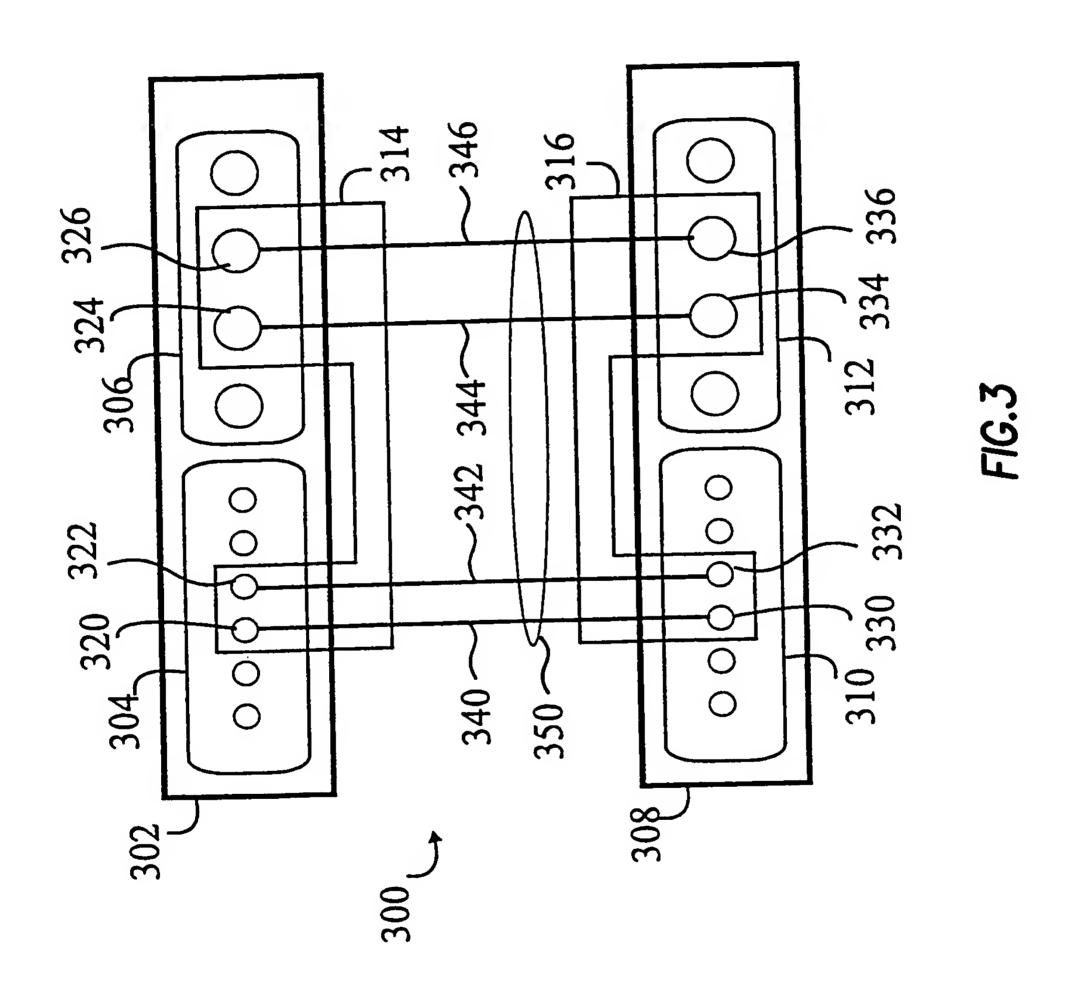


FIG.



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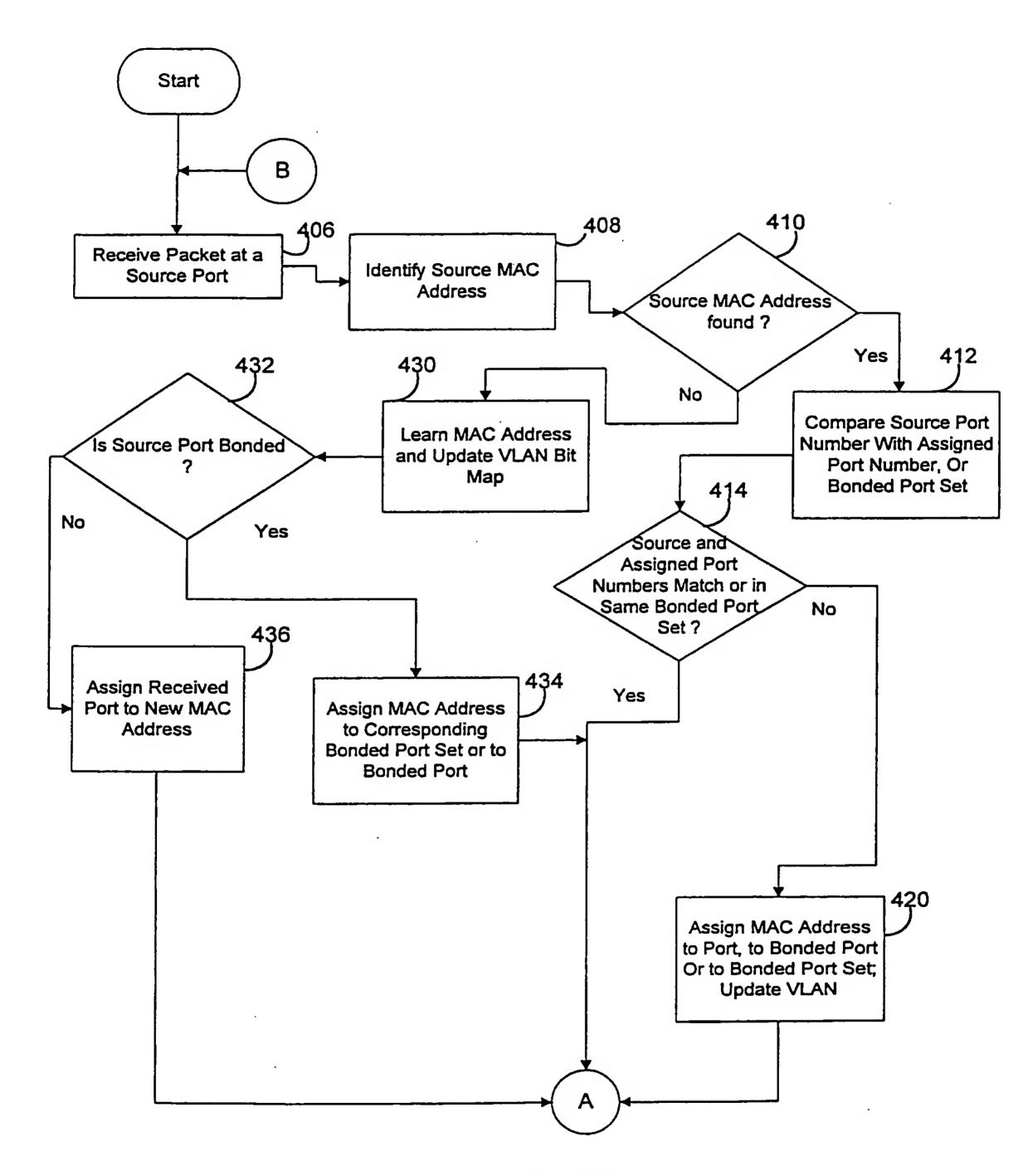


FIG.4A

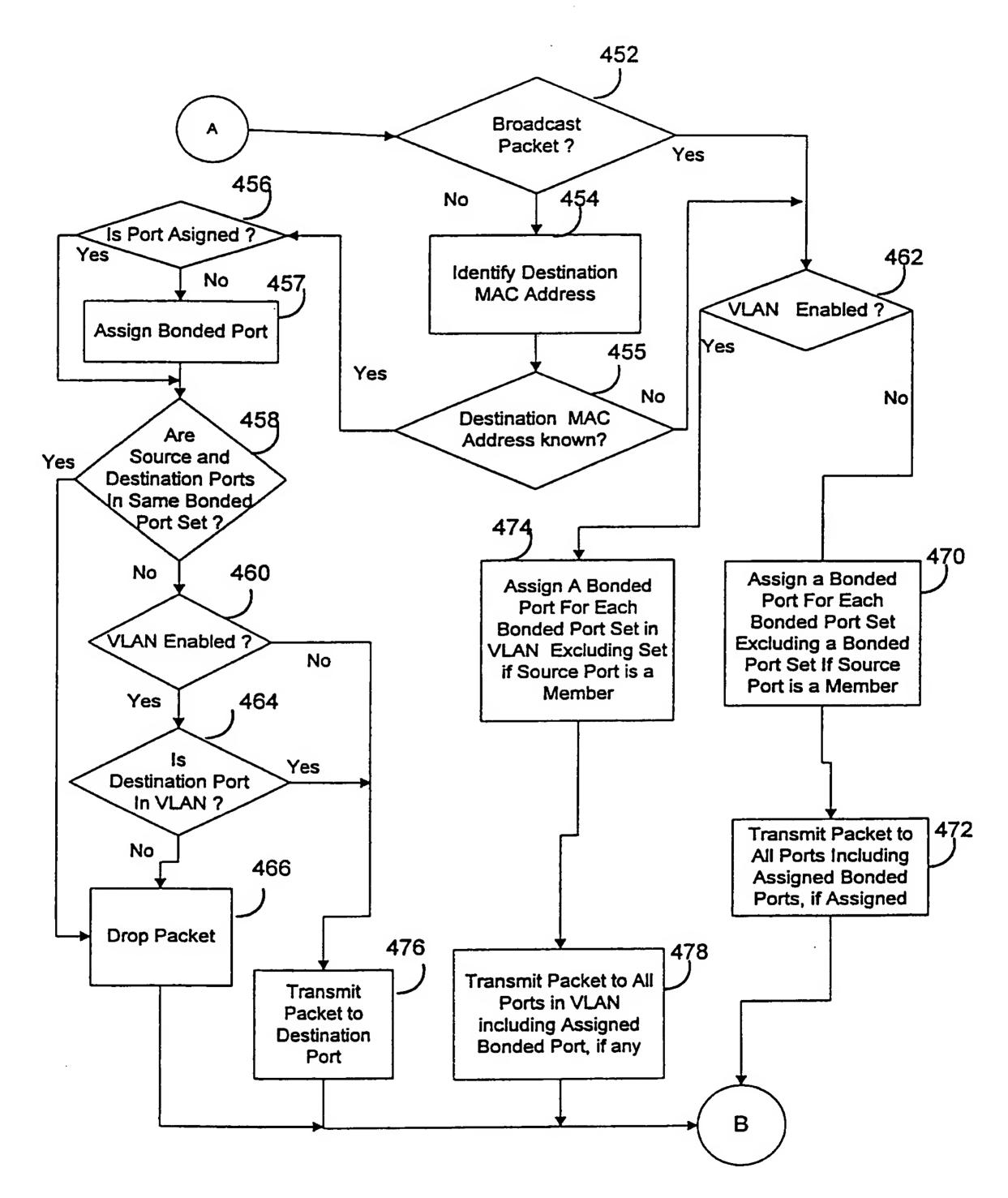
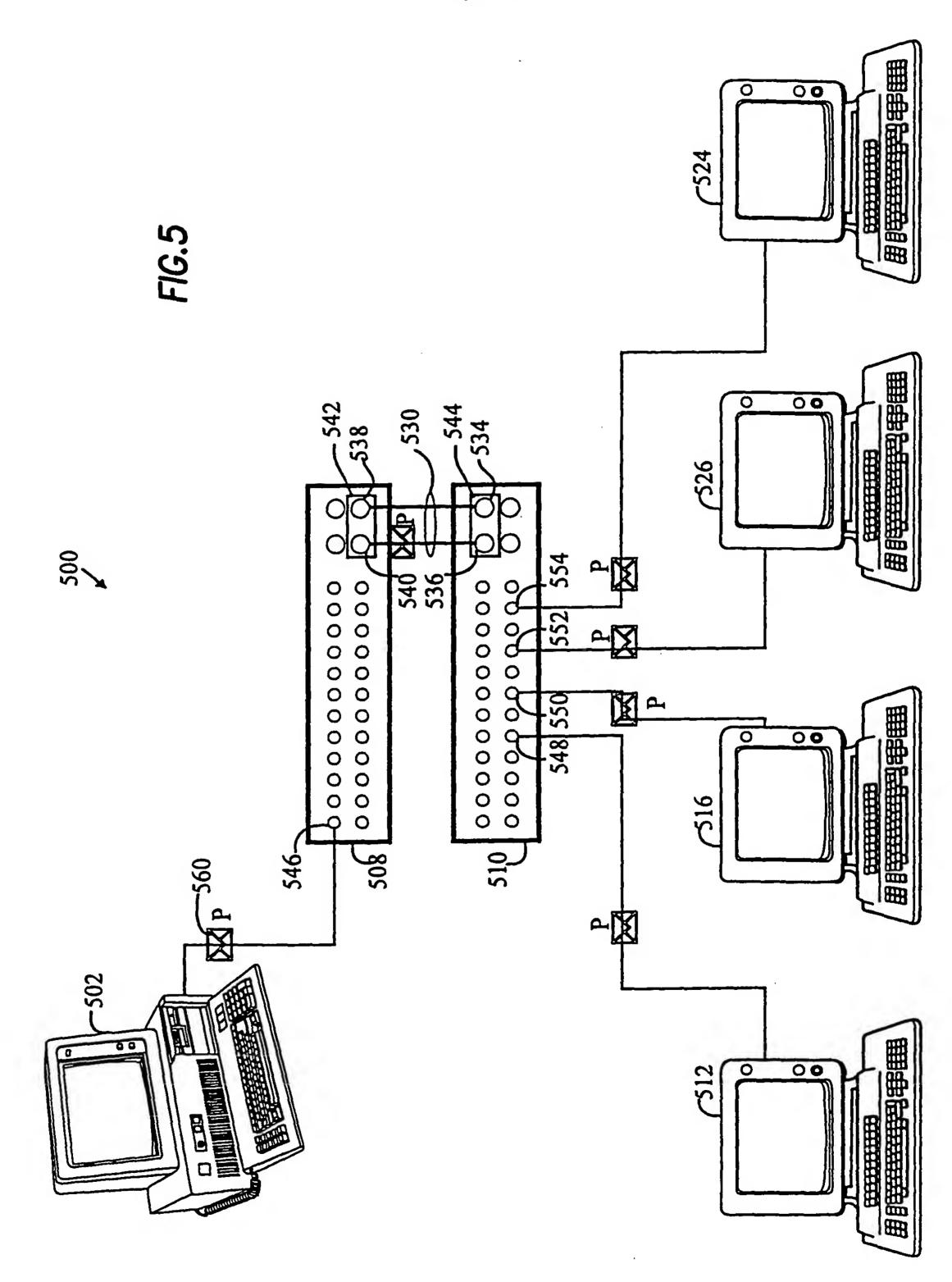
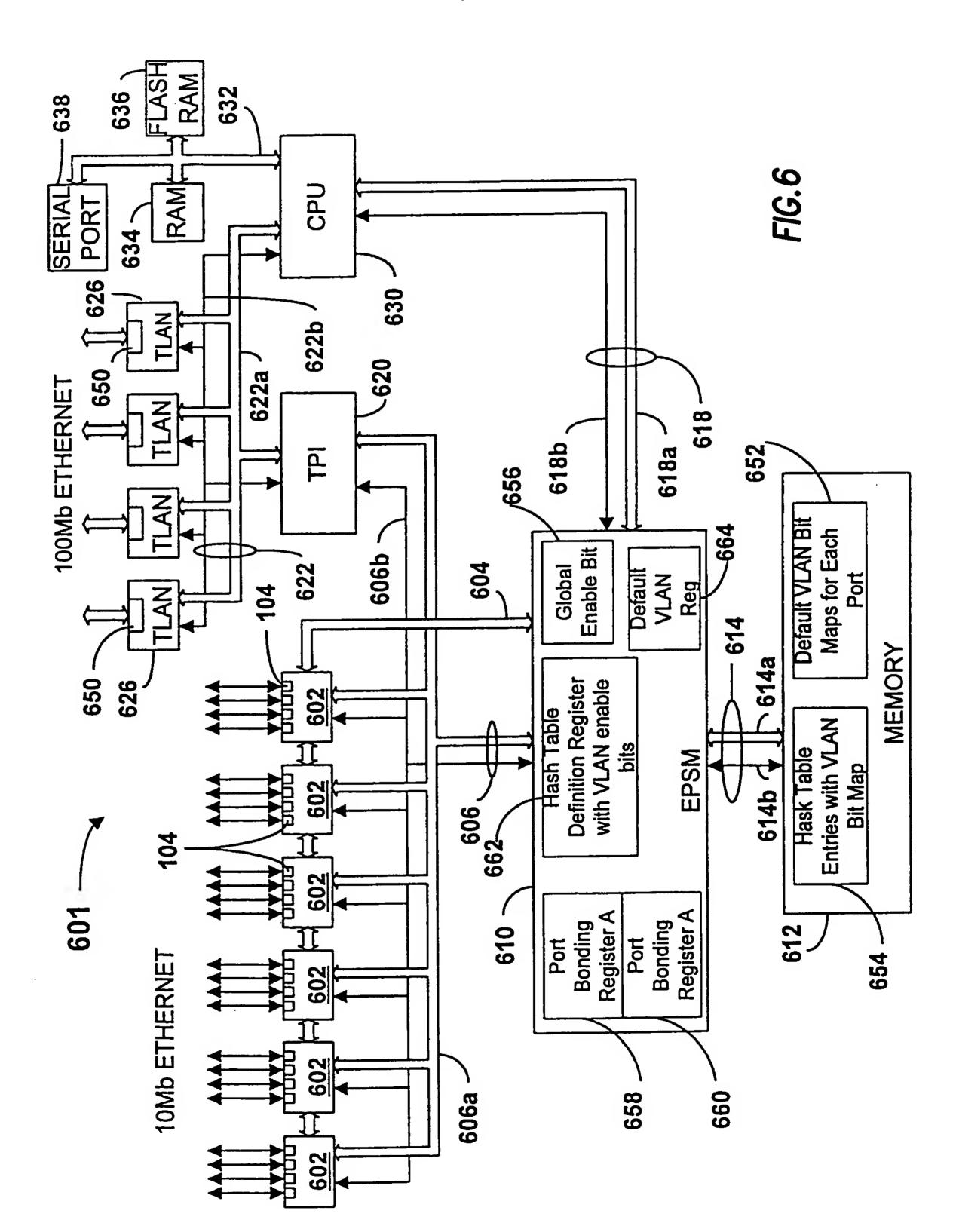


FIG.4B





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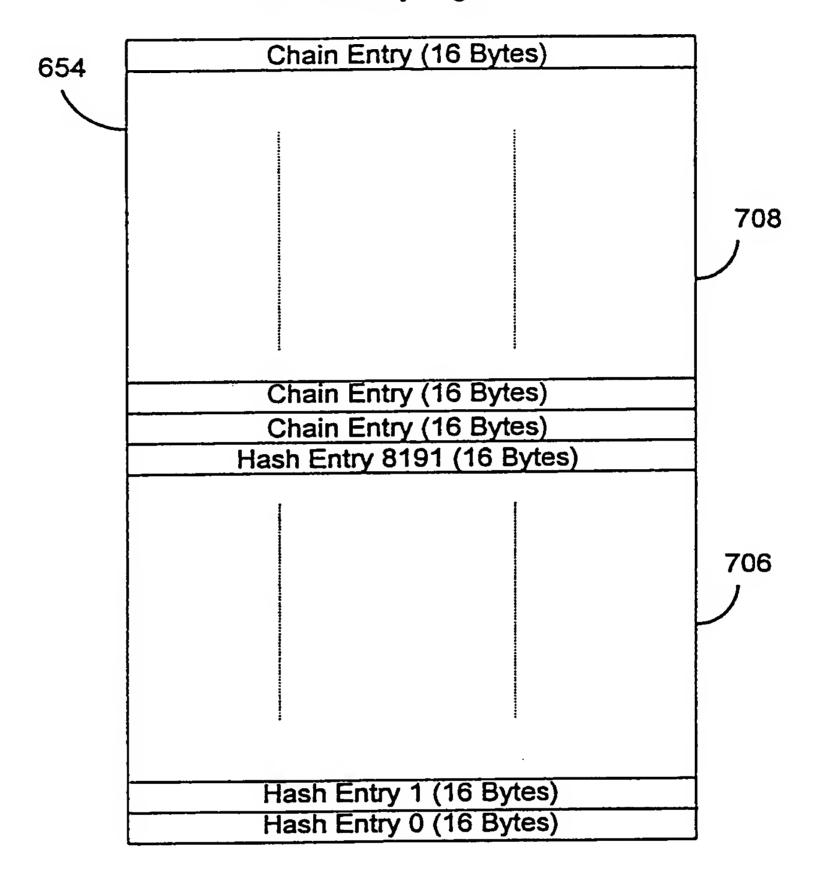
612 <u>704</u> PACKET MEMORY Default VLAN Bit Maps <u>652</u> <u>654</u> HASH TABLE

FIG.7A

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FIG.7B

Hash Memory Organization



	•		710	
Link A31-24	Link A23-16	Link A15-8	Link A7-4,000	Byte15-12
VLAN byte 3	VLAN Byte 2	VLAN Byte 1	VLAN Byte 0	Byte11-8
Control / Age	Port Num	Address Byte 5	Address Byte 4	Byte 7-4
Address Byte 3	Address Byte 2	Address Byte 1	Address Byte 0	Byte 3-0

FIG.7C

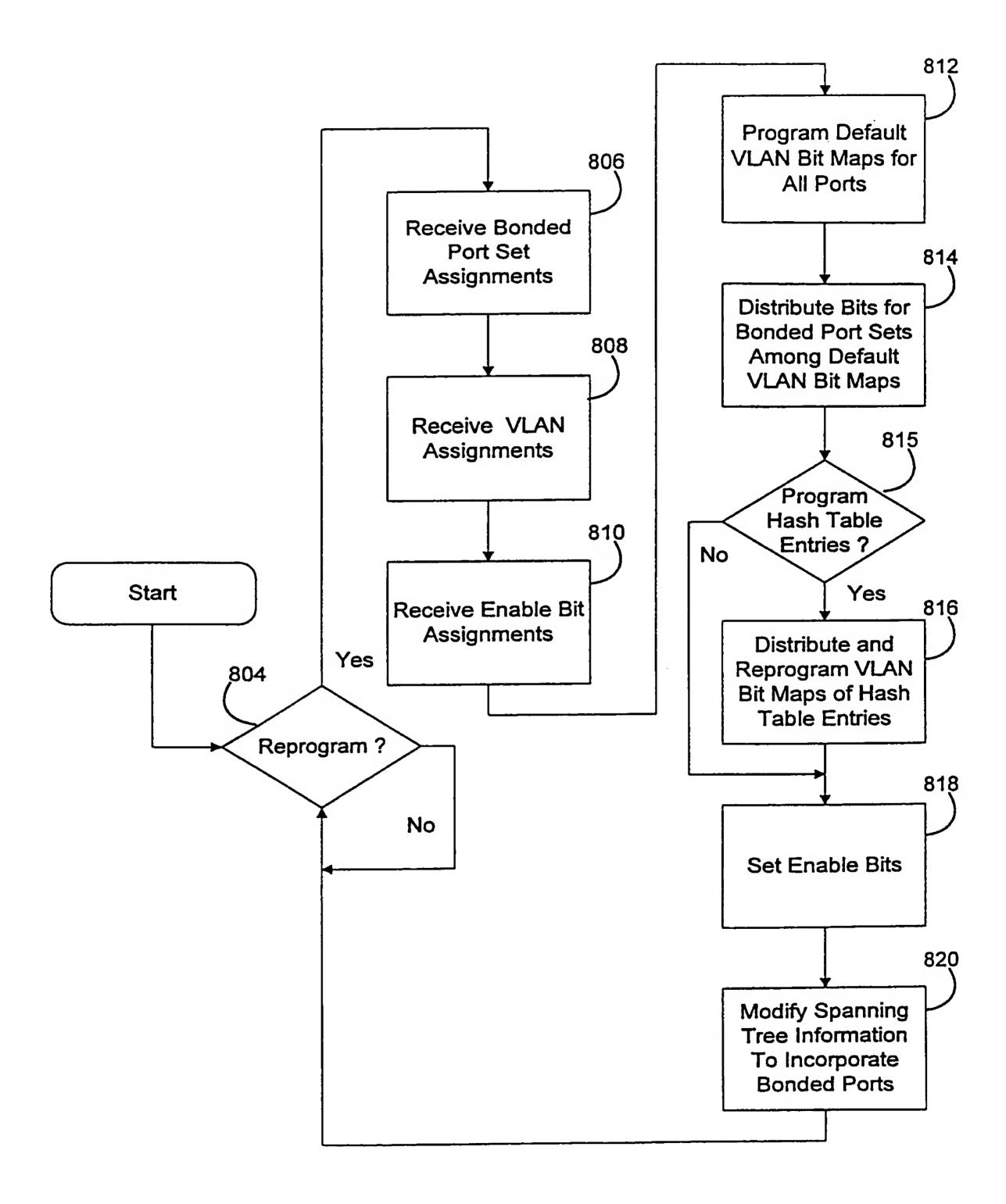


FIG.8

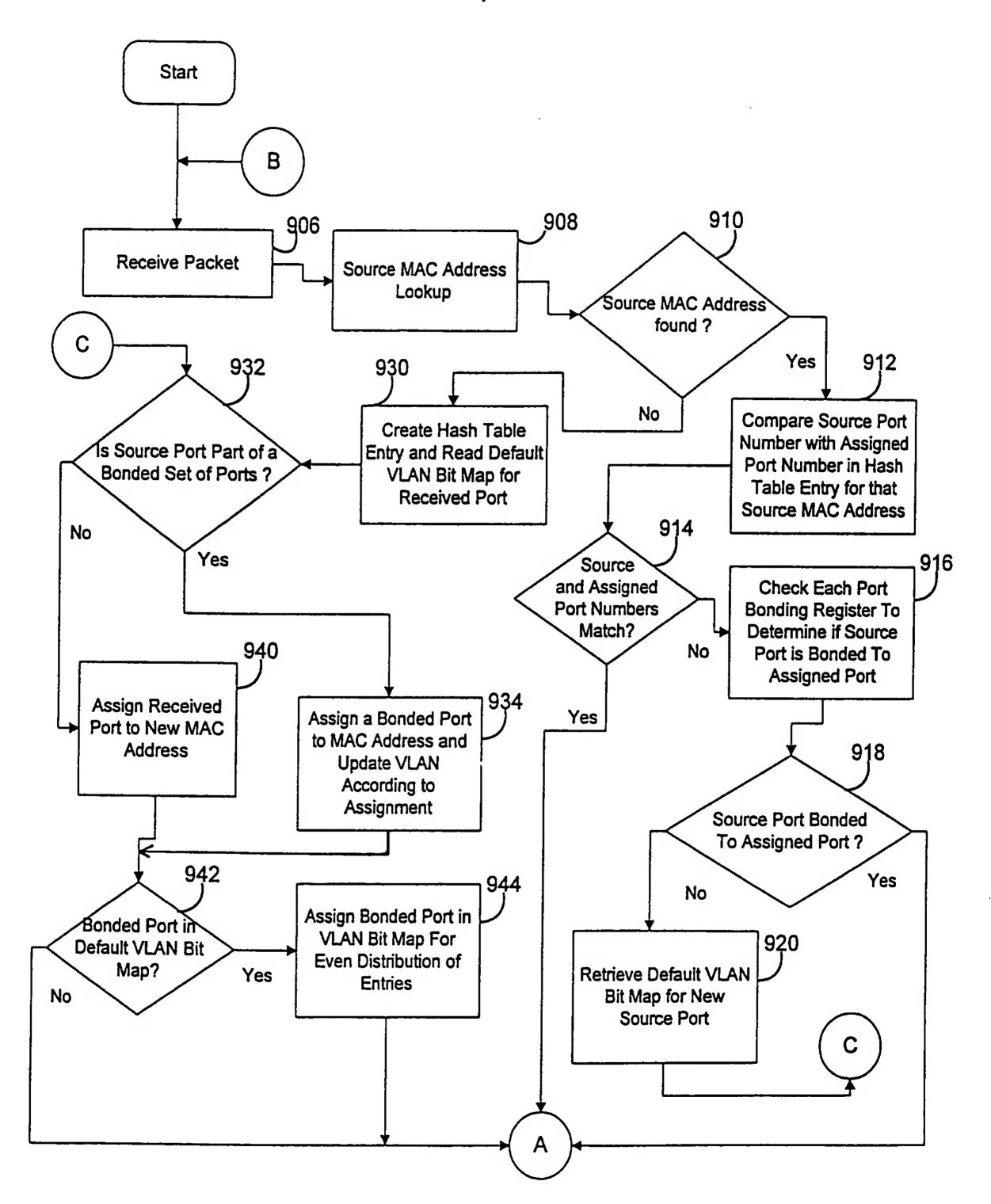


FIG.9A

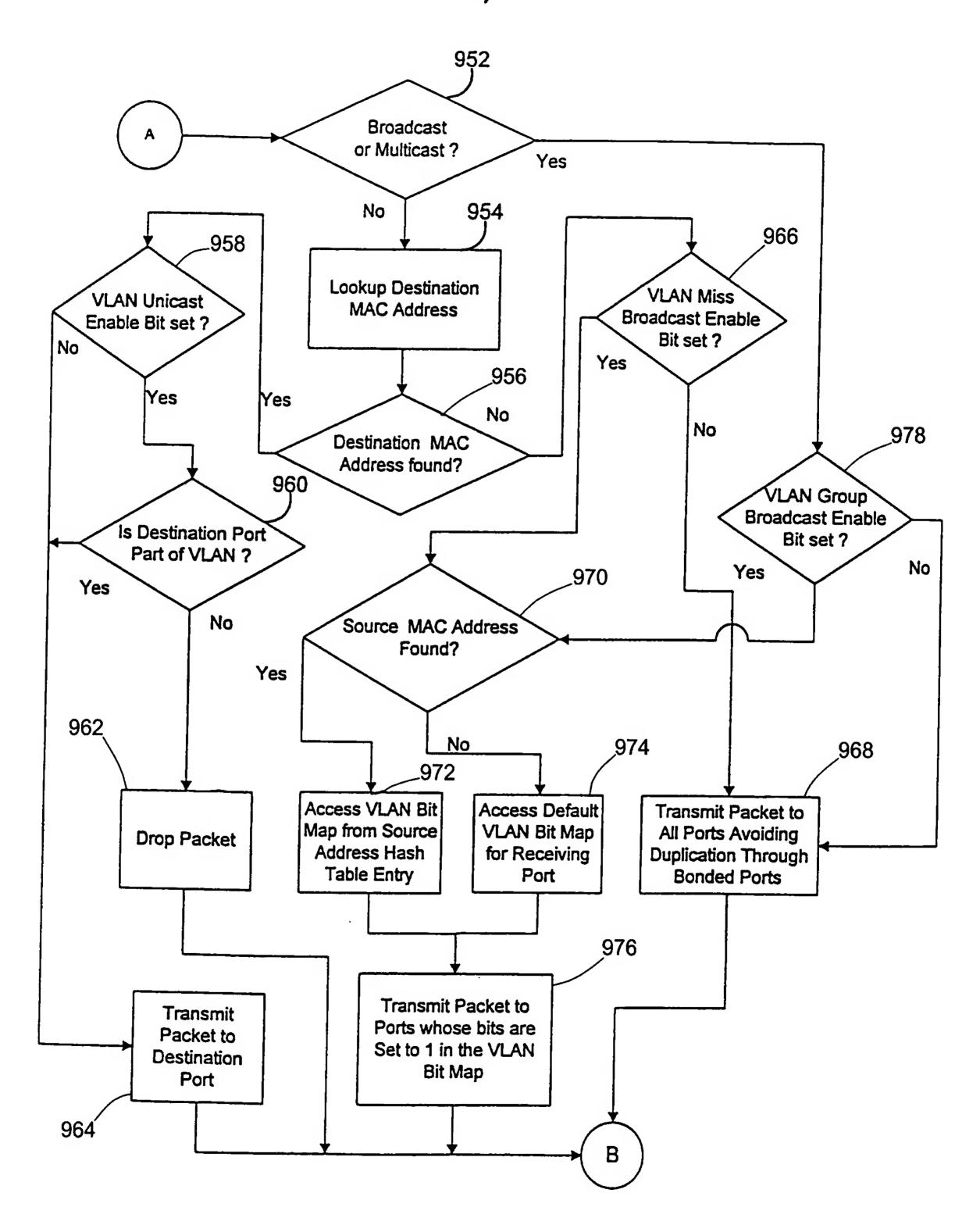


FIG.9B

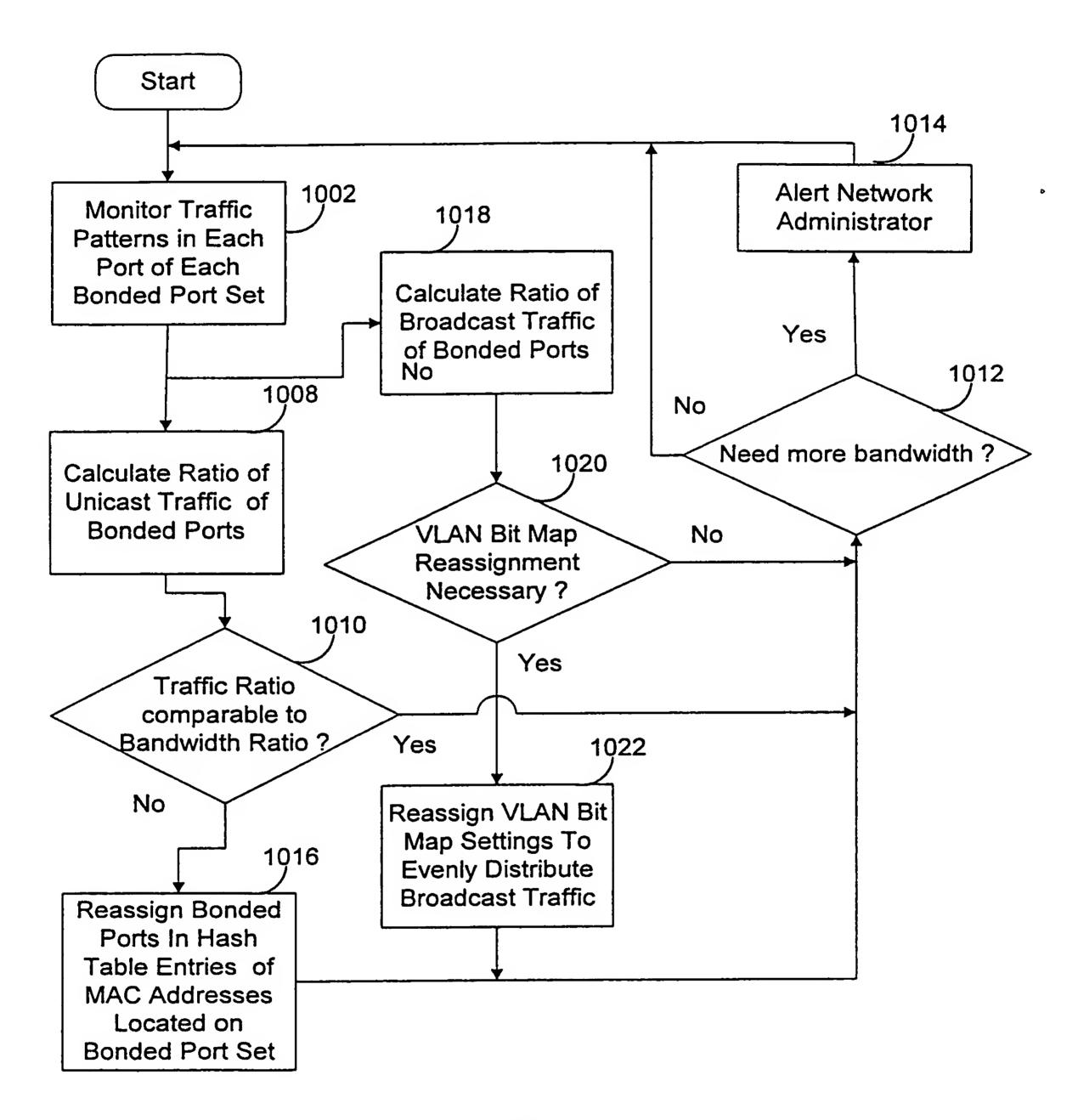


FIG. 10